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L1.clm.	2

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L2 L1.clm. 2 L2

L1 DMA near10 ASIC 104 L1

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Terms	Documents
L1 and (without adj3 access\$3)	25

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result set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

<u>L3</u>	L1 and (without adj3 access\$3)	25	<u>L3</u>
<u>L2</u>	L1 and (without adj2 access\$3)	22	<u>L2</u>
<u>L1</u>	DMA near10 ASIC	324	<u>L1</u>

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Search Results -

Terms	Documents
L3	0

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<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L4</u>	L3	0	<u>L4</u>
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L3</u>	L1 and (without adj3 access\$3)	25	<u>L3</u>
<u>L2</u>	L1 and (without adj2 access\$3)	22	<u>L2</u>
<u>L1</u>	DMA near10 ASIC	324	<u>L1</u>

END OF SEARCH HISTORY

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Search Results -

Terms	Documents
(709/208 709/212 710/22 710/300 710/308 710/72 710/74 710/240 710/241 710/113 711/100 711/202).ccls.	6782

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Search:

L5

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result set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L5 710/22,300,308,72,74,240,241,113;711/100,202;709/208,212.ccls.

6782 L5

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L4 L3

0 L4

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L3 L1 and (without adj3 access\$3)

25 L3

L2 L1 and (without adj2 access\$3)

22 L2

L1 DMA near10 ASIC

324 L1

END OF SEARCH HISTORY

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Search Results -

Terms	Documents
L3 and L5	12

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L6

Refine Search

Recall Text 

Clear

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Set Name Query

side by side

Hit Count Set Name

result set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L6 L3 and L512 L6L5 710/22,300,308,72,74,240,241,113;711/100,202;709/208,212.ccls.6782 L5

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L4 L30 L4

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L3 L1 and (without adj3 access\$3)25 L3L2 L1 and (without adj2 access\$3)22 L2L1 DMA near10 ASIC324 L1

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2	BRS	L2	24	11 same first same	USPA	2005/08/2				
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			B1			for controlling packet		709/238
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6802023	20041005	23	Redundant controller	714/7	714/4
			B2			data storage system hav		
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6776544	20040817	31	Arrangement for	400/120.01	101/71;
			B2			printing a print image		101/91;
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6739245	20040525	25	Method for controlling	101/91	101/71;
			B2			printing in a mail-proc		358/1.1;
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6733194	20040511	23	Arrangement for	400/120.01	101/71;
			B2			controlling printing in		101/91;
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6715101	20040330	22	Redundant controller	714/11	
			B2			data storage system hav		
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6708285	20040316	21	Redundant controller	714/11	
			B2			data storage system hav		
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6298370	20011002	173	Computer operating	718/102	718/100
			B1			process allocating task		
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6179489	20010130	173	Devices, methods,	718/102	
			B1			systems and software or		
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6125398	20000926	119	Communications	709/236	370/263;
			A			subsystem for computer-		370/431
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IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

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1-25 | 26-28

- ☐ 1. **The Equator MAP-CA/spl trade/ DSP: an end-to-end broadband signal processor/spl trade/ VLIW**
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- ☐ 2. **A 110-K transistor 25-MPixels/s configurable image transform processor unit**
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- ☐ 3. **Evaluation of the Raw microprocessor: an exposed-wire-delay architecture for ILP and streams**
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- ☐ 4. **QCDOC: A 10 Teraflops Computer for Tightly-Coupled Calculations**
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- ☐ 5. **A pipelined low power architectural MPEG-4 video codec chip with deblocking filter for mobile wireless multimedia applications**
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